



One Week
AICTE Sponsored
Online Faculty Development Program

on

“Trends & Issues on VLSI and Communication Engineering”

from

25th Oct.- 30th Oct, 2021

Patron

Prof. B.R. Kamboj
Vice-Chancellor, GJUS&T, Hisar

University FDP Coordinator

Dr. Sandeep K. Arya
Professor, Department of ECE, GJUS&T

Chairperson

Dr. Deepak Kedia
Professor, Department of ECE

Program Coordinator

Mr. Vinod Kumar
Assistant Professor, Department of ECE
Email: vinodspec@gjust.org
Ph: 9468462423 (M)

Organized by:

Department of Electronics & Communication Engineering
Guru Jambheshwar University of Science & Technology, Hisar, Haryana-125001

About the University

Guru Jambheshwar University of Science and Technology, Hisar-125001, Haryana started its journey on October 20, 1995 under Haryana State Legislative Act No. 17 of 1995. The University has been recognized by the University Grants Commission (UGC) under Section 2(f) for recognition of degrees since 11.1.1996 and under section 12(B) of the UGC Act to be eligible for central assistance since 7.2.1997. The university aims to impart education on the frontiers of Engineering and Technology, Pharmacy, Environmental Studies, Non-conventional Energy Sources, Mass Media and Management Studies. Today the university is rock standing on 372 acres of lush green land with 23 Teaching Departments. The University at present offers 55 Regular Programmes on campus including B.Tech., M.Tech., B.Pharma., M.Pharma., B.Physiotherapy, M.Physiotherapy, M.Sc., M.B.A, M.Com and M.C.A. etc. with Ph.D. degree programme in all the Departments. The University is 'A' Grade accredited by National Assessment and Accreditation Council (NAAC), since 2002. The University has been ranked 94th among the Universities/ Institutes in India as per National Institute Ranking Framework (NIRF). It has also been ranked between 6-25 in ATAL Ranking of Institution of Innovation and Achievement (ARIIA) 2020 and in between 801-1000 in World University Ranking 2021 by Times Higher Education, UK. The university has also secured rank between 251-300 in the globally renowned Times Higher Education Young University Rankings 2021. The h-index (Scopus) of the university has gone upto 92 at present. For more detail please visit: <http://www.gjust.ac.in>



About the Department

The Department of Electronics and Communication Engineering was established in year 2001 duly approved by AICTE. The department is committed to impart technical education in the most efficient manner to its students. Within a short span of its existence the department has equipped itself with most of modern facilities required for achieving excellence such as well-established library, ultra-modern laboratory supported by rich core faculty. During few years of existence only, the department has established itself as well-known entity in field of Electronics and communication engineering. The department offers B.Tech. in ECE with an intake of 60 students/year, M.Tech. with an intake of 20

students/year and Ph.D. The B.Tech. program of department is also accredited by NBA.

About AICTE Scheme

A MOU was signed between AICTE and our esteemed university for conduct of joint training program for teachers and non-teaching staff of AICTE approved technical institutions affiliated with university. The main aim of this scheme is to help in imparting quality technical education in the country and to support technical institutions in fostering research, innovation and entrepreneurship through training in various emerging areas.

Program Monitoring Committee

- ❖ Col. B. Venkat, Director, FDP Cell, AICTE
- ❖ Prof. Saroj, Dean, FET, GJUS&T, Hisar
- ❖ Prof. Yogesh Chaba, Subject Expert, CSE
- ❖ Prof. Dharmender Kumar, Chairperson, CSE, GJUS&T, Hisar
- ❖ Prof. Deepak Kedia, Chairperson, ECE, GJUS&T, Hisar
- ❖ Prof. Sanjeev Kumar Dhull, Subject Expert, ECE, GJUS&T, Hisar
- ❖ Dr. Vinod Kumar, Librarian
- ❖ Prof. Om Parkash, Director DDE & Professor, CSE
- ❖ Dr. Priti Prabhakar, Chairperson, EE, GJUS&T, Hisar
- ❖ Mr. Mukesh Arora, Head UCIC
- ❖ Prof. Sandeep K. Arya, university FDP, Coordinator

Faculty Development Program Objectives

With the advent of very large scale integration (VLSI) designs, the number of applications of integrated circuits (ICs) in high-performance computing, controls, telecommunications and consumer electronics has been rising at a very fast pace. Keeping in view of this, the department of Electronics & Communication Engineering is going to organize one week faculty development program on “Trends & Issues on VLSI and Communication Engineering” from 25-10-2021 to 30-10-2021 through online mode. The main objective of the course is to discuss the current Trends & Issues on VLSI and Communication Engineering. It provides an opportunity to highlight recent trends and developments in the field of communication engineering. This course is fully sponsored by AICTE to train Faculty Members, Research scholars, PG students of AICTE affiliated engineering colleges/institutions.

Registration Form Link:

<https://forms.gle/YuYSCvQTCL5MaD4U9>

Last date of registration: **22nd Oct., 2021**

Registration Fee: There will be **No registration fee** for attending the above faculty development program.

E-Certificate will be provided to those having 80% Attendance, 60% marks in the Online Test and have submitted feedback forms.